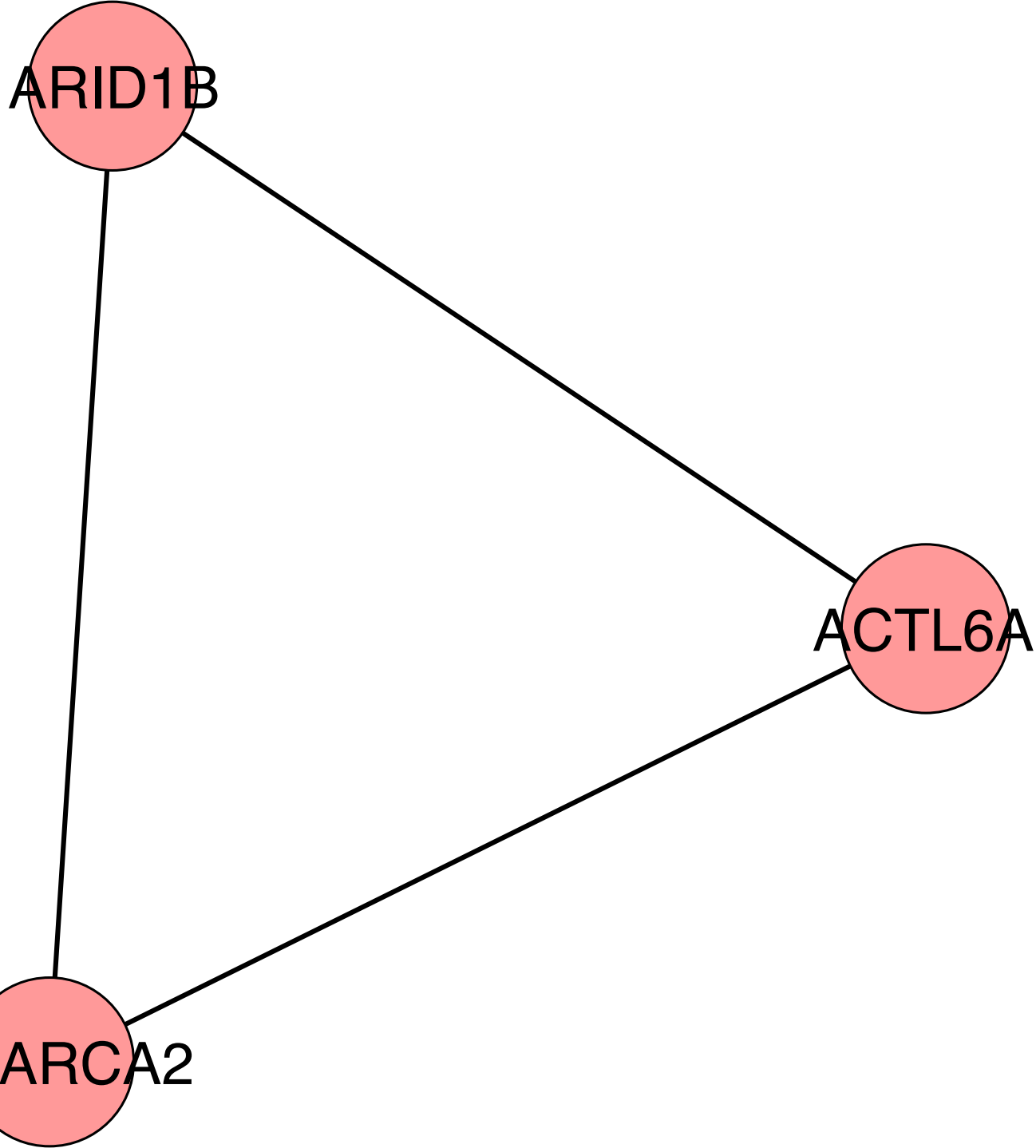


ARID1B



```
graph TD; ARID1B --- SMARCA2; ARID1B --- ACTL6A; SMARCA2 --- ACTL6A
```

SMARCA2

ACTL6A

